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			Application Number	10/811,835	+
TRANSMITTAL FORM		Filing Date		╁	
			03/30/2004	-	
(to be used for all correspondence after initial filing)		First Named Inventor	Kazuaki GOTO et al.	H	
		Group Art Unit	2825	L	
		Examiner Name	Yelena Rossoshek	Ļ	
Total Number of Pages in This Submission		Attorney Docket Number	030712-29	L	
ENCLOSURES (check all that apply)					
			nent Papers	After Allowance Communication to Grou	.
Fee Attached		Drawing	(s)	Appeal Communication to Board of Appeals and Interferences	
Amendment / Reply		_ `	tion and Power of Attorney	Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)	į
☐ After Final		Licensing-related Papers		Proprietary Information	ĺ
Affidavits/declaration(s)		Petition		Status Letter	
Extension of Time Request		Petition to Convert to a Provisional Application Power of Attorney, Revocation		Application Data Sheet Request for Corrected Filing Receipt with	
Express Abandonment Request				Enclosures	1
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Document(s)				response to restriction requirement	
Response to Missing Parts/ Incomplete Application		CD, Nu	mber of CD(s)		
Response to Missing Parts under 37 CFR 1.52 or 1.53					
		Remarks F			T
		The Commissioner is hereby authorized to charge any additional fees required or credit any overpayments to Deposit Account No. 19-2380 for the			
above identified docket number.					
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT Firm Donald R. Studebaker, Reg. No. 32,815					
or Individual name	Nixon Peak		Reg. No. 32,815		
401 9th Street, N.W.					
	Suite 900 Washington	n, D.C. 200	04-2128		
Signature	Dence				
Date	October 12, 2007				
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Kazuaki GOTO et al.

) Group Art Unit: 2825

Serial No. 10/811,835

) Examiner: Yelena Rossoshek

Filed: 03/30/2004

) Date: October 12, 2007

OF A SEMICONDUCTOR DEVICE

) CERTIFICATE OF MAILING OR TRANSMISSION

[37 CFR 1.8(a)]

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RESPONSE TO RESTRICTION/ELECTION REQUIREMENT

Mail Stop Non-Fee Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action September 13, 2007, Applicants hereby elect Group I, claims 1-5 and 10-15, directed to a method of designing a circuit layout of a semiconductor integrated circuit, for prosecution on the merits.

Should the Examiner believe a conference would be of benefit in expediting the prosecution of the instant application, she is hereby invited to telephone counsel to arrange such a conference.

Respectfully submitted,

Donald R. Studebaker Reg. No. 32,815

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